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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\*****\*\* FOREIGN APPLICATIONS \*\*\*\*\***JAPAN JP2003-085314 03/26/2003 ~  
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Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	6	18	7
Verified and Acknowledged Examiner's Signature	Initials				

**ADDRESS**

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**TITLE**

Analog-to-digital conversion circuit and image processing circuit for stepwise conversion of a signal through multiple stages of conversion units

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
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